

ABSTRACT OF THE DISCLOSURE

A method for compressing a semiconductor integrated circuit, comprising dividing a design region, in which a semiconductor integrated circuit is to be designed, into a plurality of blocks, assigning
5 semiconductor devices to each of the blocks, determining a device density of each block, compressing any block, and connecting the blocks by wiring. In the compressing of the block, the block that is determined
10 to have a low device density is compressed.